



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of: Jitaru
Serial #: 09/434,985
Filed: 11/05/99

Docket #: 1675B.1A.1
Examiner:
Group:

For: Low Noise Full Integrated Multilayers Magnetic For Power
Converters

FEE CALCULATION
FOR THIRD PRELIMINARY AMENDMENT

Honorable Commissioner of Patents and Trademarks
Washington D.C. 20231

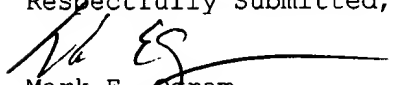
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Sir:

Payment is based on the following calculations (37 C.F.R. 1.16):

CURRENT NUMBER OF INDEPENDENT CLAIMS	<u>6</u>	
NUMBER OF INDEPENDENT CLAIMS PREVIOUSLY PAID	<u>5</u>	
Excess:	<u>1</u>	\$ <u>39.00</u>
(X) Small Entity @ \$39.00 each		
() NOT Small Entity @ \$78.00 each		
CURRENT NUMBER OF CLAIMS	<u>32</u>	
NUMBER OF CLAIMS PREVIOUSLY PAID	<u>23</u>	
Excess	<u>9</u>	\$ <u>81.00</u>
(X) Small Entity @ \$9.00 each		
() NOT Small Entity @ \$18.00 each		
CURRENT NUMBER OF MULTIPLE DEPENDENT CLAIMS	<u>0</u>	
NUMBER OF MULTIPLE INDEPENDENT CLAIMS PAID	<u>0</u>	
Excess	<u>0</u>	... \$ <u>0.00</u>
(X) Small Entity @ \$130.00		
() NOT Small Entity @ \$260.00		
TOTAL:		\$ <u>120.00</u>

Respectfully Submitted,


Mark E. Ogram
Attorney at Law
Reg. No. 30343

Date: may 19, 2000

CERTIFICATE OF MAILING (37 CFR 1.8)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on may 19, 2000


Mark E. Ogram, reg. no. 30343

Date may 19, 2000



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Sir:

Regarding the above identified patent application, please make the following amendments and note the corresponding remarks.

Enclosures:

- (1) FEE CALCULATION sheet;
- (2) Check number 3533 for the amount \$ 120.00.

In the Claims:

Add the following claims ¹⁶⁻²⁴ ~~24-32~~:

- 1 - - ²⁰ 1. The power processing device according to claim ¹⁸ 22, further
2 including an active element secured to said multilayered printed circuit board
3 and connected to the first set of electrically conductive windings. - -
- 1 - - ²¹ 2. The power processing device according to claim ²⁰ 24, further
2 including:
3 a) a heat sink connected to one face of said multilayered printed
4 circuit board opposing said active element; and,
5 b) a thermal via thermally connecting said active element and the heat
6 sink. - -

06/02/2000 SLUANG1 00000035 09434985

01 FC:202 39.00 OP
02 FC:203 81.00 OP